

Date	2015/07/ 31	SR#	1-2181047221
Category	■FAQ □SOP	Related OS	N/A
Abstract	ADAM DO High-To-Low & Low-to-High Delay Definition		
Keyword	ADAM, DO, Digital Output, High-To-Low Delay, Low-to-High Delay		
Related Product	All ADAM Series with DO support		

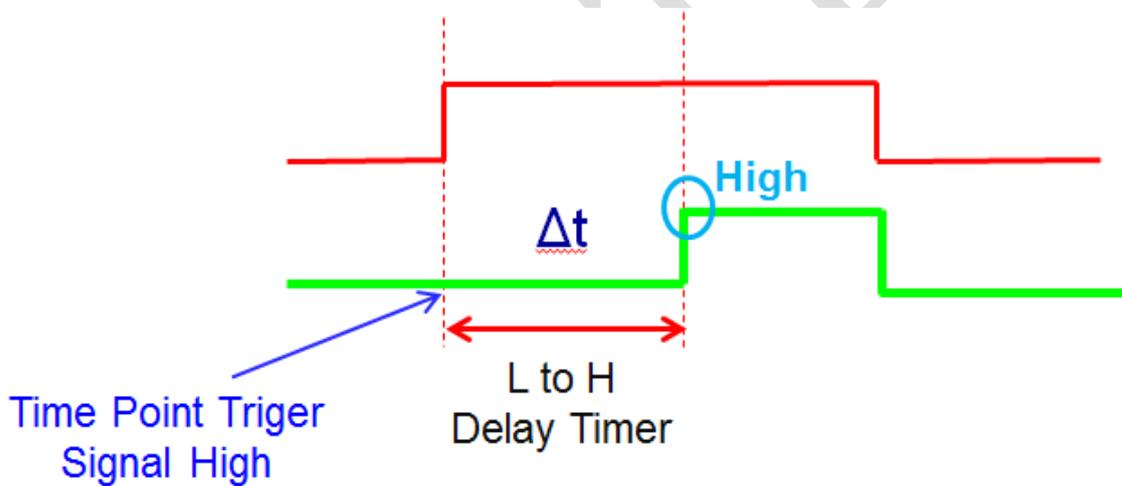
■ **Problem Description:**

This document explains a common question regarding the definition of ADAM DO High-To-Low & Low-to-High Delay.

■ **Brief Solution - Step by Step:**

[Low-To-High Delay Output Mode](#)

Define how much delay time of a pulse should wait before transit to logic high



[High-to-Low Delay Output Mode](#)

Define how much delay time of a pulse should wait before transit to logic low

