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=====
Parameter Define
=====
```

```
EC_Command_Port = 0x29A
EC_Data_Port = 0x299
Write EC HW ram = 0x89
Watch dog event flag = 0x57
Watchdog reset delay time = 0x5E (high byte), 0x5F (low byte)
Reset event = 0x04
Start WDT function = 0x28
Stop WDT function = 0x29
Reset WDT function = 0x2A
```

```
=====
Sample Code (WDT 10 sec. reset event)
=====
```

```
.model small
.486p
.stack 256
.data
.code
org 100h
.STARTUp

mov dx, EC_Command_Port
mov al,89h    ; Write EC HW ram.
out dx,al

mov dx, EC_Data_Port
mov al, 5Fh    ; Watchdog reset delay time low byte (5Eh is high byte) index, Timebase: 100ms
out dx,al

mov dx, EC_Data_Port
mov al, 64h    ;Set 10 seconds delay time.
out dx,al

mov dx, EC_Command_Port
mov al,89h    ; Write EC HW ram.
```

```
out dx,al
```

```
mov dx, EC_Data_Port
```

```
mov al, 5Eh ; Watchdog reset delay time low byte (5Eh is high byte) index, Timebase: 100ms
```

```
out dx,al
```

```
mov dx, EC_Data_Port
```

```
mov al, 00h ;Set 10 seconds delay time.
```

```
out dx,al
```

```
mov dx, EC_Command_Port
```

```
mov al,89h ; Write EC HW ram.
```

```
out dx,al
```

```
mov dx, EC_Data_Port
```

```
mov al, 57h ; Watch dog event flag.
```

```
out dx,al
```

```
mov dx, EC_Data_Port
```

```
mov al, 04h ; Reset event.
```

```
out dx,al
```

```
mov dx, EC_Command_Port
```

```
mov al,28h ; start WDT function. (Stop: 0x29, Reset: 0x2A)
```

```
out dx,al
```

```
.exit
```

```
END
```