

Enabling an Intelligent Planet

Data Create	2015/04/07	Release Note	Internal External		
Category	FAQ	Product Group	IAG		
Function	Registry Map	Related OS	NA		
Related Product	UNO-3085G				

[Abstract]

How to use RU tool to check Registry Map of PCIe-to-PCI Bridge IC

[Solution]

- 1. Download RU.exe tool.
- 2. Go into RU.exe in DOS environment.
- 3. Press "F6", then you can see the device list. Each manufacturer has a unique Vendor ID. Take PCIe-to-PCI bridge IC as an example. Vendor ID 10B5 represents PLX company.

File C	onfig Go Tools System Quit	An	American Megatrends, Inc.						
Nane		Vendor	Device	Bus#	Dev#	Fun#			
D0:F0	Intel Host Processor Bridge	8086	0104	00	00	00			
D2:F0	Intel VGA	8086	0106	00	02	00			
D25:F0	Intel Ethernet	8086	1502	00	19	00			
D26:F0	Intel USB	8086	1C2D	00	18	00			
027:FØ	Intel Multi-media(PCI Express)	8086	1020	00	18	80			
128 : FA	Intel PCI Bridge(PCI Express)		1010	00	10	00			
028:F6	Intel PCI Bridge(PCI Express)		1010	00	10	06			
129 · FØ	Intel USB	8086	1026	00	10	00			
131.10	Intel ISA Bridge	8086	1049	00	1F	00			
D31.F0	Intel INF	8086	1001	00	1F	02			
DJI-16 DJI-16	Intel Serial Bus Controller	8086	1022	00	1F	03			
D31-13	Intel INF	8086	1009	00	1F	05			
U31:15	DCL Pridge(PCL Fynress)	1085	8112	01	00	00			
NA:10	PLI Briugetrui Express?	8086	1070	82	0F	00			
D15:F0 D0:F0	Intel Ethernet(PCI Express)	8086	10D3	03	00	00			

4. Go in to PCI Bridge device. You can see the registry map.



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Del Europe, Del Del Deldes																	
rui	ryhr	Eas		1.01	U	T.		1 10	ıye								
F7D	0-Mer	ioru	j Li	imit													
002	2 00	01	02	03	04	05	06	07		09	ØA				ØE		Refresh : ON
000	0 B5	10	12	81	07		10		AA		04	06	10		01		VID:DID = 10B5:8112
001	0 OC			FØ					01	02	02	20	EØ	EØ	20	22	Rev ID : AA
002	0 D 0	F7	(DØ	F7:	FØ	FF											Int Line (IRQ): OB
003			11	0100	300								0B	01	10		Int Pin : 01
004	0 01	50	02	5A	00	80			13	28	ØE						M64: F000000C 00010000
005	0 05	60	80	80	00	00							00			00	Mem: 00000000 00000010
006	0 10	00	71	88			90	05		20	ØA	00	11	4C	02	00	Pri/Sec/Sub: 01/02/02
007	0 00	00	11	00	80	0C					40				00	00	I/O Base : FFFFE000
008	0 00	88	80	88	00		00		33					00	00	00	I/O Limit : FFFFEFFF
009	0 00	00	00	00			80		00		00			00		00	Mem Base : F7D00000
ØØA	0 00	00	00	00	00							00	80	00	00	90	Men Limit : F7DFFFFF
OOB	0 00			88	88	00	00	00	00	00	00				00	00	Prefetchable Memory:

5. In the Databook of PCI bridge IC. There will be a table to explain the meaning of Registry Map.

Table 15-6.	Forward Bridge Mode	PCI-Compatible	Configuration	(Type 1) Register Map
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PCI Configuration Register Offset	31 24	23 16	15 8	7 0				
00h	PCI De	vice ID	PCI Vendor ID					
04h	PCI S	PCI Co	mmand					
08h		PCI Device Revision ID						
0Ch	PCI Built-In Self-Test (Not Supported)	PCI Header Type	PCI Bus Latency Timer	PCI Cache Line Size				
10h								
14h								
18h	Secondary Latency Timer	Subordinate Bus Number	Secondary Bus Number	Primary Bus Number				
1Ch	Seconda	I/O Base						
20h	Memory Limit Memory Base							
24h	Prefetchable 1	Memory Base						
28h	Prefetchable Memory Base Upper 32 Bits							
2Ch	Prefetchable Memory Limit Upper 32 Bits							
30h	I/O Limit Upper 16 Bits I/O Base Upper 16 Bits							
34h	Reserved PCI Capability Point							
38h	PCI Base Address for Expansion ROM (Not Supported)							
3Ch	Bridge Control PCI Interrupt Pin PCI Ir							