

Data Create	2015/04/07	Release Note	<input type="checkbox"/> Internal <input checked="" type="checkbox"/> External
Category	FAQ	Product Group	IAG
Function	Registry Map	Related OS	NA
Related Product	UNO-3085G		

[Abstract]

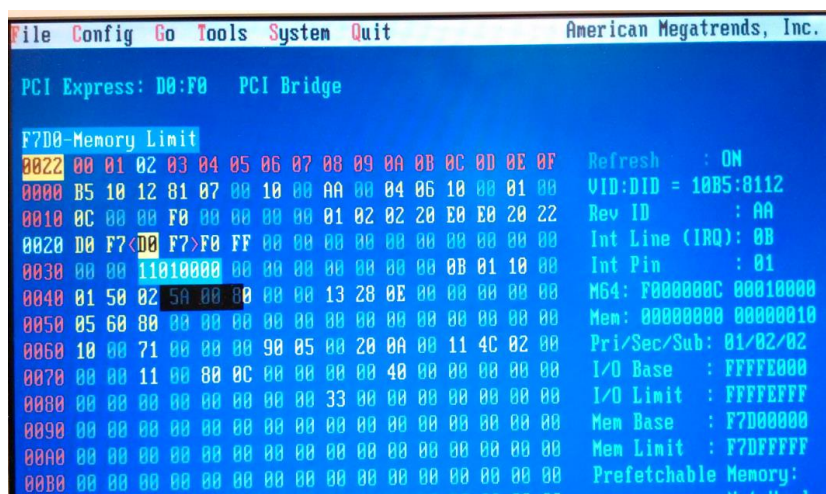
How to use RU tool to check Registry Map of PCIe-to-PCI Bridge IC

[Solution]

1. Download RU.exe tool.
2. Go into RU.exe in DOS environment.
3. Press "F6", then you can see the device list. Each manufacturer has a unique Vendor ID. Take PCIe-to-PCI bridge IC as an example. Vendor ID 10B5 represents PLX company.

Name	Vendor	Device	Bus#	Dev#	Fun#
00:F0 Intel Host Processor Bridge	8086	0104	00	00	00
02:F0 Intel VGA	8086	0106	00	02	00
025:F0 Intel Ethernet	8086	1502	00	19	00
026:F0 Intel USB	8086	1C2D	00	1A	00
027:F0 Intel Multi-media(PCI Express)	8086	1C20	00	1B	00
028:F0 Intel PCI Bridge(PCI Express)	8086	1C10	00	1C	00
028:F6 Intel PCI Bridge(PCI Express)	8086	1C1C	00	1C	06
029:F0 Intel USB	8086	1C26	00	1D	00
031:F0 Intel ISA Bridge	8086	1C49	00	1F	00
031:F2 Intel IDE	8086	1C01	00	1F	02
031:F3 Intel Serial Bus Controller	8086	1C22	00	1F	03
031:F5 Intel IDE	8086	1C09	00	1F	05
00:F0 PCI Bridge(PCI Express)	10B5	0112	01	00	00
015:F0 Intel Ethernet	8086	107C	02	0F	00
00:F0 Intel Ethernet(PCI Express)	8086	10D3	03	00	00

4. Go in to PCI Bridge device. You can see the registry map.



5. In the Databook of PCI bridge IC. There will be a table to explain the meaning of Registry Map.

Table 15-6. Forward Bridge Mode PCI-Compatible Configuration (Type 1) Register Map

PCI Configuration Register Offset	31	24	23	16	15	8	7	0
00h	PCI Device ID				PCI Vendor ID			
04h	PCI Status				PCI Command			
08h	PCI Class Code						PCI Device Revision ID	
0Ch	PCI Built-In Self-Test <i>(Not Supported)</i>		PCI Header Type		PCI Bus Latency Timer		PCI Cache Line Size	
10h	PCI Base Address 0							
14h	PCI Base Address 1							
18h	Secondary Latency Timer		Subordinate Bus Number		Secondary Bus Number		Primary Bus Number	
1Ch	Secondary Status				I/O Limit		I/O Base	
20h	Memory Limit				Memory Base			
24h	Prefetchable Memory Limit				Prefetchable Memory Base			
28h	Prefetchable Memory Base Upper 32 Bits							
2Ch	Prefetchable Memory Limit Upper 32 Bits							
30h	I/O Limit Upper 16 Bits				I/O Base Upper 16 Bits			
34h	Reserved						PCI Capability Pointer	
38h	PCI Base Address for Expansion ROM <i>(Not Supported)</i>							
3Ch	Bridge Control				PCI Interrupt Pin		PCI Interrupt Line	